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APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/014,553	12/14/2001	Masud Jenabi	46417.001012	6016		
7590 03/03/2004			EXAM	EXAMINER		
Stephen T. Schreiner, Esq.			ISSING, GR	ISSING, GREGORY C		
Hunton & Will Suite 1200	liams	ART UNIT	PAPER NUMBER			
1900 K Street,	N.W.	3662	3662			
Washington, DC 20006			DATE MAILED: 03/03/2004			

Please find below and/or attached an Office communication concerning this application or proceeding.

1		Application	on No.	Applicant(s)				
,		10/014,55	i3	JENABI, MASUD	7			
	Office Action Summary	Examiner	/	Art Unit				
		Gregory C		3662				
Period fo	The MAILING DATE of this communicat or Reply	tion appears on the	cover sheet with the c	orrespondence addr	0SS			
A SHOTHE I - Exter after - If the - If NO - Failui Any r	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICA asions of time may be available under the provisions of 37 SIX (6) MONTHS from the mailing date of this communic period for reply specified above is less than thirty (30) da period for reply is specified above, the maximum statute to reply within the set or extended period for reply will, eply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	TION. 7 CFR 1.136(a). In no everation. ays, a reply within the statury period will apply and will by statute, cause the appl	ent, however, may a reply be tir story minimum of thirty (30) day Il expire SIX (6) MONTHS from ication to become ABANDONE	nely filed s will be considered timely. the mailing date of this commod (35 U.S.C. § 133).	munication.			
Status								
1)🖂	Responsive to communication(s) filed o	n 12 December 20	003.					
•	_ ·	☐ This action is n						
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Dispositi	on of Claims							
5)□ 6)⊠ 7)□	Claim(s) 1-10 and 12-33 is/are pending 4a) Of the above claim(s) 12-22 is/are we claim(s) is/are allowed. Claim(s) 1-10 and 23-33 is/are rejected Claim(s) is/are objected to. Claim(s) are subject to restriction	vithdrawn from con	sideration.	•				
Applicati	on Papers							
9)[The specification is objected to by the Ex	xaminer.						
10) 🔲 🤄	10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
	Applicant may not request that any objection	-,,	•	` '				
11)	Replacement drawing sheet(s) including the The oath or declaration is objected to by				• •			
Priority u	ınder 35 U.S.C. § 119							
a)[Acknowledgment is made of a claim for All b) Some * c) None of: 1. Certified copies of the priority doc 2. Certified copies of the priority doc 3. Copies of the certified copies of the application from the International see the attached detailed Office action for	cuments have bee cuments have bee he priority docume Bureau (PCT Rule	n received. n received in Applicati ents have been receive e 17.2(a)).	ion No ed in this National St	age _.			
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	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-	948)	4) Interview Summary Paper No(s)/Mail D					
3) 🔲 Inform	nation Disclosure Statement(s) (PTO-1449 or PTC r No(s)/Mail Date		5) Notice of Informal F 6) Other:		52)			

1. This application contains claims 12-21 drawn to an invention nonelected with traverse in Paper No. 6 (7/18/03). A complete reply to the final rejection must include cancelation of nonelected claims or other appropriate action (37 CFR 1.144) See MPEP § 821.01.

- The following is a quotation of the second paragraph of 35 U.S.C. 112:
 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claim 10 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
- 4. Claim 10 remains unclear with respect to a "chip is using a multifunction self-aligned gate process." The statements by applicant do not correct the lack of clarity; how does a chip use a multifunction self-aligned gate process? The alleged support (Andricos) merely describes that the chips are manufactured by said process. The terminology "manufactured by" and "using" are not the same.
- 5. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 6. Claims 1, 2, 6-9, 11, 22-26, 28-31 and 33 are rejected under 35 U.S.C. 102(b) as being anticipated by Fassett et al.
- 7. The rejection is previously set forth in the last Office Action.
- 8. Applicant alleges that Fassett et al fail to disclose the use of phase bits 110 and 112 to control scan angle. Applicant alleges that there is nothing to suggest that the polarization switch 18a provides scan angle control, only selectable polarization states. Therefore, Applicant alleges Fassett also fails to disclose controlling the phase shifters.

over Fassett et al.

9. Claims 3-5 and 10, 27 and 32 are rejected under 35 U.S.C. 103(a) as being unpatentable

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10. The rejection is previously set forth in the last Office Action.

Applicant merely alleges that he does not agree with the rejection but fails to provide any support as to why the rejection is not proper. Applicant also alleges that the reference to Fassett et al is an improper primary reference since Fassett et al allegedly fail to teach the elements of claim 1.

Response to Arguments

12. Fassett et al is directed to devices for controlling the phase and polarization of microwave signals wherein as is known in the art a collimated beam of radio frequency can be formed and steered by controlling the phase of the energy radiated from each one of a plurality of antenna elements in an array (col. 1, lines 8-19). The switches 18a-18n provide phase shift and polarization control wherein as is known, radio frequency from a transmitter is collimated into a beam and directed in accordance with commands from a beam steering computer (col. 3, lines 1-17). The phase shifter/polarization switch is comprised of a plurality of serially connected phase bits that are effective to provide in combination a four bit phase shifter and a polarization switch (col. 3, lines 39-50). Figure 1 clearly shows beam steering computer 32 controlling the phase shifter/polarization switches. Additionally, once a particular polarization is selected, the phase delay is set (see col. 8, lines 4-12). Thus, applicant's argument that Fassett et al does not control scan angle is contrary to the teachings of the reference as well as to any phased array antenna and is unfounded. The antenna array of Fassett et al is clearly directed to a directional antenna array

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having a beam steering computer for controlling the phase shifts, which as is known in the art and disclosed therein forms and steers the RF energy.

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- 13. With regard to the obviousness of the specific features, it is clear that Fassett et al teach phase resolution of 22.5° wherein each of the resolution bits is half of the preceding, i.e. 180°, 90°, 45°, and 22.5°. To provide a greater resolution at the expense of greater cost via more bits is obvious, i.e. more phase bits of 11.25° and 5.625°. The formation of beams an a phased array antenna conventionally controls the amplitude as well as the phase to form the beam, thus control of amplitude is clearly within the skill of the artisan and the scope of the phased array antenna of Fassett et al. Therefore, applicant's arguments are not convincing.
- 14. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gregory C. Issing whose telephone number is (703)-306-4156. The examiner can normally be reached on Mon-Thurs 6:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas Tarcza can be reached on (703)-306-4171. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Gregory C. Issing
Primary Examiner
Art Unit 3662

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